

Appl. No. 10/605,327  
Amdt. dated December 15, 2004  
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### AMENDMENTS TO THE SPECIFICATION

After paragraph [0011], please add new paragraph [0011.1]:

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Fig.5 illustrates an adjustable impedance circuit wherein the impedance devices are inductors according to another embodiment of the present invention.

After paragraph [0011.1], please add new paragraph [0011.2]:

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Fig.6 illustrates an adjustable impedance circuit wherein the impedance devices are capacitors according to another embodiment of the present invention.

In paragraph [0015]:

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Please notice that although the first impedance 42 and the second impedance 46 are resistors in the embodiments mentioned above, they could also be other impedance devices such as capacitors and inductors. For example, Fig.5 shows an adjustable impedance circuit 500 wherein the first impedance 502 comprises a first inductor L1, and the second impedance 504 comprises a second inductor L2; and Fig.6 shows an adjustable impedance circuit 600 wherein the first impedance 602 comprises a first capacitor C1, and the second impedance 604 comprises a second capacitor C2. Additionally, although the first impedance 42 and the second impedance 46 in the embodiments mentioned above are implemented by at least one transmission gate or at least one MOS transistor, other devices which can achieve the same purpose also belong to the present invention.

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